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CONFIRMATION NO. 7396

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| APPLICANTS Satoshi Ishikura, Osaka, JAPAN; | | | | | | | | | | |
| ** CONTINUING DATA ********************************** | | | | | | | | | | |
| ** FOREIGN APPLICATIONS ************************************ | | | | | | | | | | |
| ** 09/24/2003 Foreign Priority claimed | | Ø yes □ no | | | CHI | | | | | |
| 35 USC 119 (a-d) conditions | | | | STATE OR COUNTRY JAPAN | DRA | SHEETS DRAWING 16 | | MS | CLAIMS | |
| ADDRESS 00116 PEARNE & GORDON LLP 1801 EAST 9TH STREET SUITE 1200 CLEVELAND, OH 44114-3108 | | | | | | | | | | |
| TITLE Semiconductor device, semiconductor device design method, semiconductor device design method recording medium, and semiconductor device design support system | | | | | | | | | | |
| | | | | | | □ All F | Fees | | | |
| | | | | | | □ 1.10 | 6 Fees (| Filing |) | |
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